

History and Technology of Microelectronics - Taiwan

Summer



ASU
Arizona State
University

Global Education Office
Ira A. Fulton Schools of Engineering

This program reviews the history of integrated circuit development starting with the invention of the transistor in 1947 through the dramatic expansion of the industry in Silicon Valley to the rise of Japan, South Korea, Taiwan and China. In addition, this course explains how transistors operate and how they are fabricated at a level suitable for all engineers, not just electrical engineers. There will be a tour of TSMC (Taiwan Semiconductor Manufacturing Corp.) and guest speakers from engineering companies and universities in Taiwan. Students will learn Chinese culture and interact with locals.



Key Details

Dates: June 1-16, 2024
Eligibility: FSE student, minimum 2.5 GPA
Application Deadline: March 1, 2024
Prerequisites: Freshman level physics and chemistry
Course: FSE 494: History and Technology of Microelectronics (3 credits)

Program Fee: \$3,825

Program fee includes: ASU tuition for 3 credits, housing, program related travel in Taiwan, academic visits & cultural activities, ASU faculty support and international health insurance.

Apply Here

<https://studyabroad.asu.edu/?go=FSETaiwan>

Funding Resources

goglobal.asu.edu/financing

Program Leaders

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